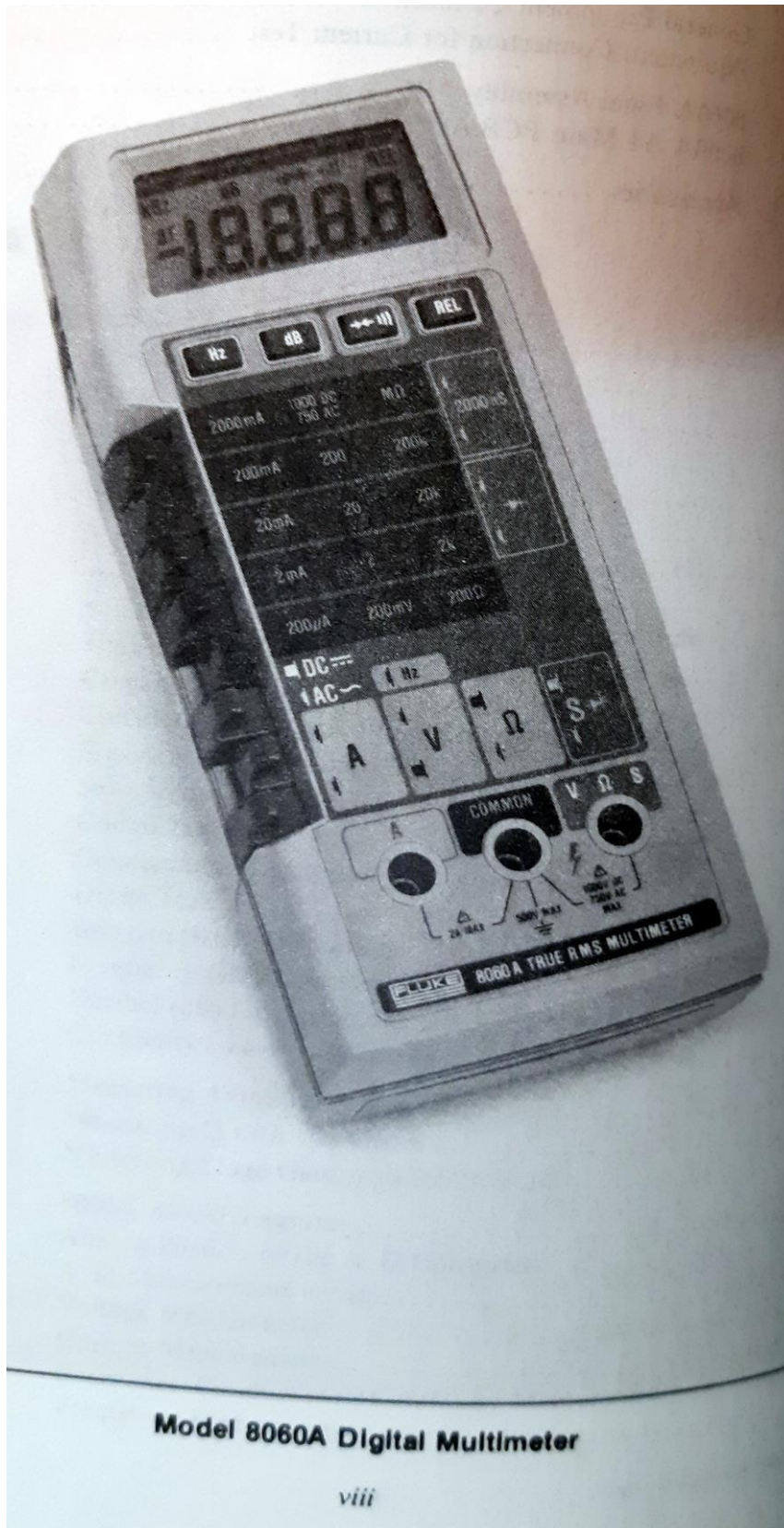


**FLUKE 8060A****RIFERIMENTI**

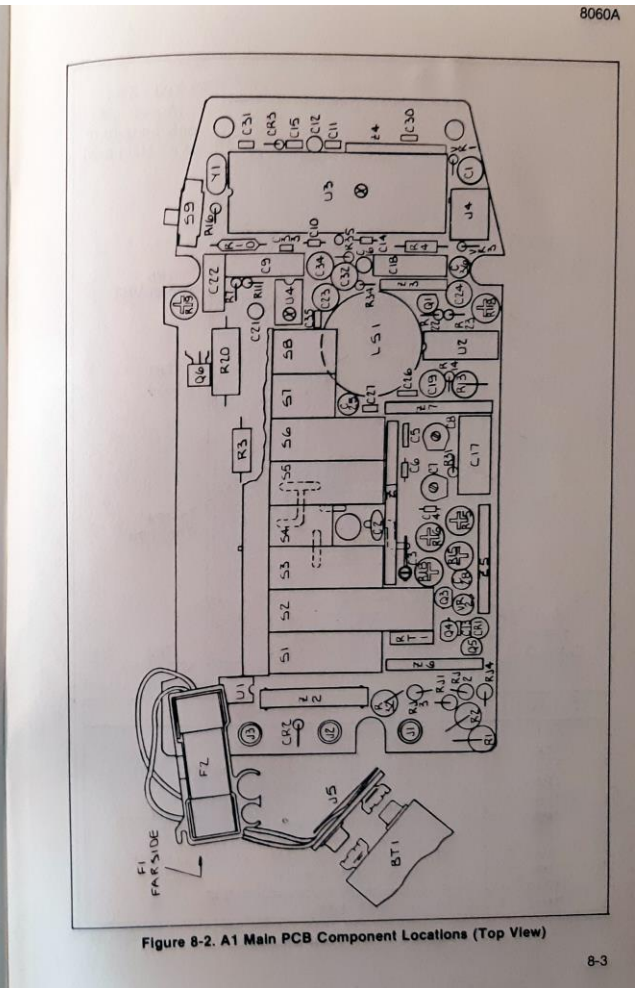
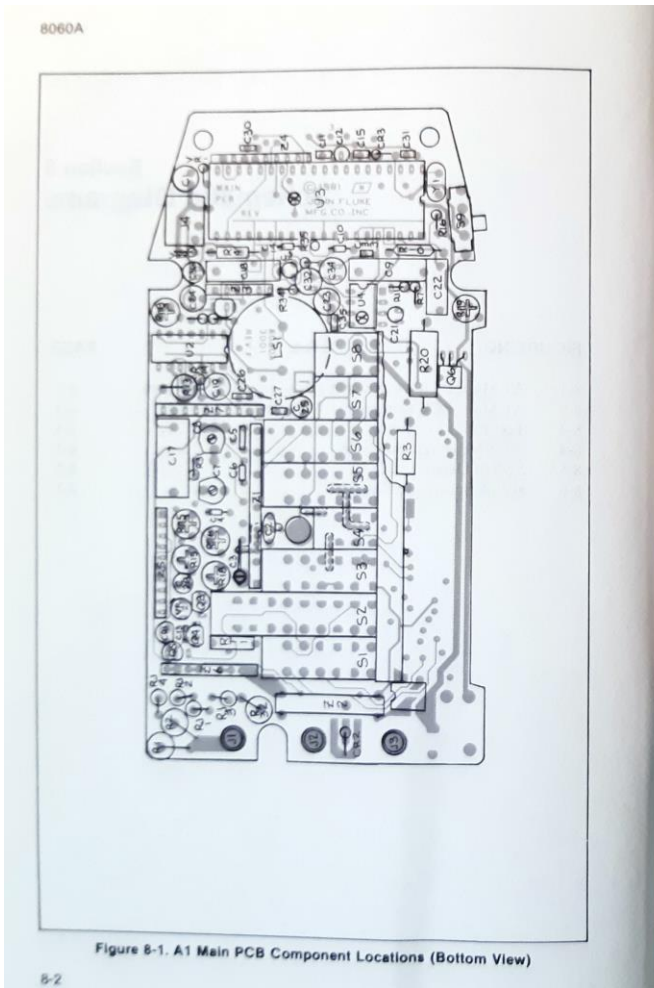
<i>Genere</i>	<i>DATA</i>	<i>Generalità</i>	<i>Note</i>	<i>Distribuzione</i>
<i>radio</i>	<i>mrz 2020</i>	<i>Schema multimetro</i>		<i>Af web</i>

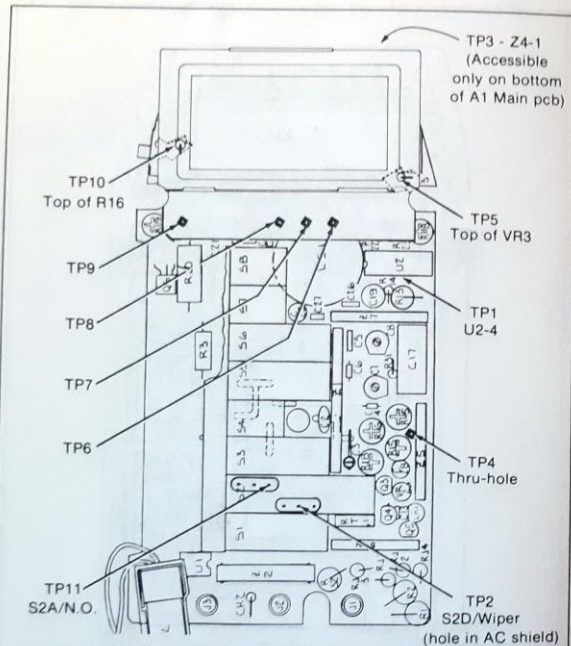




GENERALITA'

Questi DVM sono ormai fuori uso da tempo, qualcuno potrebbe trovarlo alle fiere ed aver bisogno dello schema elettrico.

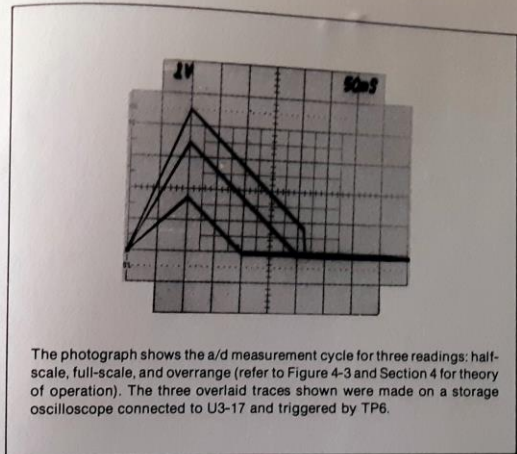




TEST POINT	DESCRIPTION
TP1	VDD, +5.2V supply.
TP2	VSS, -5.1V supply.
TP3	VDG, digital supply, +3.15V ref. to VDD (TP7)
TP4	VBG, bandgap ref., 1.2345V
TP5	Supply ground.
TP6	U5/57, scope trigger, A/D cycle
TP7	VDD, +5.2V supply (on uC pcb)
TP8	VDG, digital supply, +3.15V ref. to VDD (TP7)
TP9	uC clock, 40 kHz nominal
TP10	Freq./Continuity comparator output
TP11	Ohms Source Output

Figure 8-3. Test Point Locations

8-4



The photograph shows the a/d measurement cycle for three readings: half-scale, full-scale, and overrange (refer to Figure 4-3 and Section 4 for theory of operation). The three overlaid traces shown were made on a storage oscilloscope connected to U3-17 and triggered by TP6.

Figure 8-4. A/D Measurement Cycle

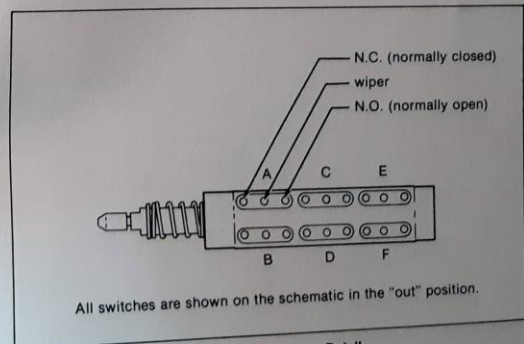


Figure 8-5. Switch Detail



8060A

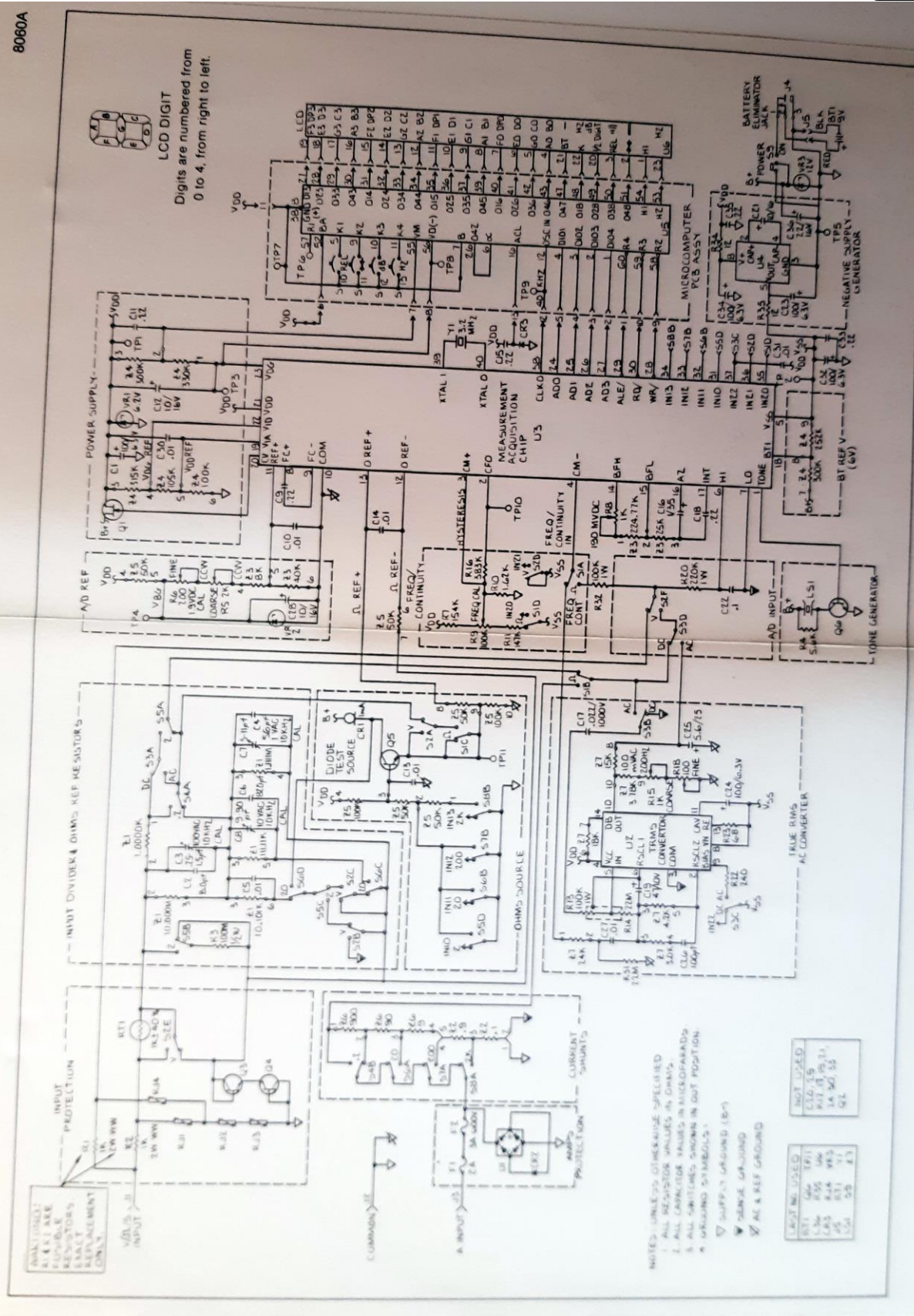


Figure 6-6. 8060A Schematic Diagram

8-5

Buon divertimento, Alessandro Frezzotti